

Use Bode Plots to Meet Dynamic-Control-Behavior Requirements

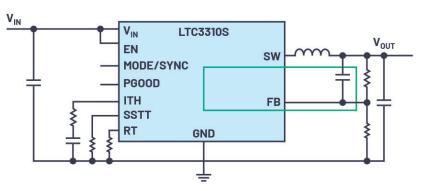
The speed and regulation stability of the control loop for a power supply's fixed output voltage can be observed and assessed with a Bode plot. Here's how it's done.

his article will describe how Bode plots can be leveraged as a tool to quickly assess if your power-supply design will meet the requirements for dynamic control behavior. Power supplies normally maintain a fixed output voltage via a control loop. This control loop may be stable or unstable. It also can regulate rapidly or slowly. In most cases, the control loop can be described via a Bode plot. With it, you're able to immediately see the speed of the control loop and especially the stability of its regulation.

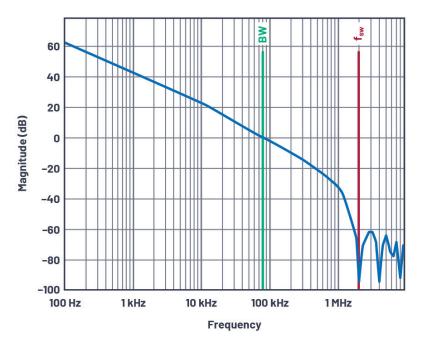
Figure 1 shows a typical switching regulator with a step-down (buck) topology. It converts a higher input voltage to a lower output voltage. The goal is to regulate the output voltage (V_{OUT}) as precisely as possible. For this, a control loop is integrated into the circuit through the feedback (FB) pin. It detects voltage changes at V_{OUT}. The control loop should respond quickly so that VOUT is always re-regulated as accurately as possible. The output voltage must be readjusted whenever the input voltage or the load current changes.

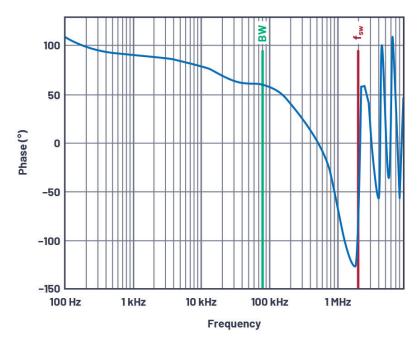
Figure 2 shows the gain curve for the control loop in a Bode plot. It yields two

2. The Bode plot shows the gain of the control loop with a 0-dB crossover point at approximately 80 kHz.



1. An example of a switching regulator that uses a control loop (shown in green) to regulate its output voltage.





3. Shown is the phase curve for the control loop with a phase margin of 60°.

important pieces of information. The frequency at which the gain is equal to 1—that is, at 0 dB—can be read. For the control loop in Figure 2, this so-called crossover frequency occurs at approximately 80 kHz. As a rule of thumb, this frequency should be no more than one-tenth of the set switching frequency of the switch-mode power supply. Unstable behavior can result if this rule is violated.

The second main piece of information shown is the area under the gain curve—the integral of the function. The higher the dc gain and the crossover frequency, the better the control loop can maintain the output voltage at a constant level.

Figure 3 shows the phase curve in a Bode plot. The most important value that can be read from this plot is the phase margin. This is a value that provides an indication of the stability of the control loop. The phase margin can be read at the crossover frequency obtained from the gain plot (Fig. 2, again). In the example shown, the crossover point lies at 80 kHz. Therefore, the phase margin in *Figure 3* is approximately 60°. Phase margins of less than approximately 40° are considered unstable.

The control loop is optimally set when the phase margin is between 40° and 70°. There's a good compromise between fast regulation and good stability in this range. Above a phase margin of 70°, systems tend to be very stable but with extremely slow regulation.

Bode plots are usually not provided in datasheets for switching regulators because the plots depend too strongly on the circuit design. The switching frequency used, the choice of external components-e.g., the inductor and

the output capacitor—and the respective operating conditions—e.g., input voltage, output voltage, and load current can have significant effects. For this reason, Bode plots often are generated using computational tools such as LTpower-CAD or with simulation tools like LTspice. With them, you can quickly find out whether a planned circuit can meet the requirements for dynamic control behavior.

Frederik Dostal studied microelectronics at the University of Erlangen in Germany. Starting work in the power-management business in 2001, he has been active in various applications positions including four years in Phoenix, Ariz., where he worked on switch-mode power supplies. He joined Analog Devices in 2009 and works as a field applications engineer for power management at Analog Devices in München.